PDMC 2005

4th International Workshop on Parallel and Distributed Methods in VerifiCation

July 10, 2005 – Lisboa, Portugal

Satellite Workshop to ICALP 2005

The growing importance of automated formal verification in industry is driving a growing interest in those aspects which have a direct impact on its applicability to real world problems. One of the main technical challenges is in devising tools that allow to handle large state spaces. Over the last years numerous approaches have been developed. Recently, an increasing interest is in parallelizing and distributing of verification techniques.

The aim of the PDMC workshop series is to cover all aspects of parallel and distributed methods and techniques for formal verification. Theoretical results, algorithms and case studies are equally welcome. Contributions from the domains of model checking, theorem proving, and equivalence checking are anticipated.

The PDMC workshop aims to provide a working forum for presenting, sharing, and discussing recent achievements in the field of parallel and distributed verification. The workshop will consist of invited talks and a selection from submitted papers.

Papers describing recent work on all aspects of parallel and distributed verification are solicited as contributions to PDMC. Topics of interest include, but are not limited to:

- parallel model checking
- distributed model checking
- distributed equivalence checking
- distributed constraints solving
- parallel methods in probabilistic model checking
- parallel methods in performance evaluation
- slicing and distributing the state space
- distributed satisfiability checking
- distributed theorem proving
- tools and case studies
- file systems for distributed transitions systems
- industrial applications

There are two categories of submissions: regular papers and presentations.

- Manuscripts of regular papers are limited to a maximum of 10 pages (excluding bibliography and technical appendices) in postscript or PDF format (ENTCS style strongly recommended).
- Presentations report on relevant results submitted to other forums or already published or on not yet finished work in progress. Presentations will appear in the workshop preliminary proceedings, but will not be considered for the final workshop proceedings. The space limit for presentations is 10 pages (excluding bibliography and technical appendices).

The preliminary workshop proceedings will be available at the meeting. The final proceedings appear as a volume of Electronic Notes in Theoretical Computer Science.

Invited speaker: Kim G. Larsen (Aalborg University, Denmark)

Program Committee:

Howard Barringer (Manchester Univ., UK), Luboš Brim (Masaryk Univ., CZ), Gianpiero Cabodi (Torino, IT), Jörg Denzinger (Alberta, Canada), Wan Fokkink (CWI Amsterdam, NL), Hubert Garavel (INRIA, FR), Jürgen Giesl (RWTH Aachen, DE), Orna Grumberg (Haifa, Israel), Boudewijn R. Haverkort (Univ. of Twente, NL), Marta Kwiatkowska (Univ. of Birmingham, UK), Martin Leucker (TU Munich, DE) – Co-chair, Eric Mercer (Brigham Young Univ., USA), Jaco van de Pol (CWI Amsterdam, NL) – Co-chair, Gerardo Schneider (Inria Rennes, FR), Willem Visser (NASA Ames Research Center, USA)

Important dates:	Submission deadline:	April 17, 2005
	Notification of acceptance:	May 16, 2005
	Presentations deadline:	May 22, 2005
	Final version:	June 3, 2005